CLAIM LISTING

- 1. (**Currently Amended**) A method for encoding data, comprising:
 - encoding a first and a second subframe of a frame of data, each subframe having multiple tracks;
 - identifying one of the multiple tracks for each subframe; and generating a track indicator to indicate to a decoder the identified track for both subframes.
- (Original) A method according to claim 1, wherein encoding the subframes having 2. multiple tracks comprises encoding subframes, each having a number of tracks, the number being other than a power of two.
- **3.** (Original) A method according to claim 2, wherein encoding the subframes having a nonpower-of-two number of tracks comprises encoding subframes having 5 tracks.
- 4. (Original) A method according to claim 1, wherein a track has pulse positions, wherein encoding subframes having multiple tracks comprises encoding subframes having at least one track with an additional pulse position as compared to another track, and wherein identifying one of the multiple tracks for each subframe comprises identifying the at least one track with the additional pulse position.
- 5. (Original) A method according to claim 1, wherein encoding the subframes comprises encoding the subframes according to the ITU-T G.729E standard.
- 6. (Original) A method according to claim 1, wherein encoding the subframes having multiple tracks comprises encoding subframes having multiple tracks in a sequence of track locations, and wherein identifying one of the multiple tracks for each subframe comprises identifying the track location of one of the multiple tracks for each subframe, and wherein

Examiner: J.B.Jeanglaude Application No.: 10/741,304 -2-Art Unit: 2819

Attorney Docket No.: 42P17107

generating the track indicator comprises generating a set of bits that corresponds to the track locations for all of the identified tracks for both subframes.

- 7. (Original) A method according to claim 6, wherein generating the set of bits comprises generating a set of bits that corresponds to an ordered pair, a value of the first member of the pair to indicate the identified track in the first subframe, and the value of the second member of the pair to indicate the identified track in the second subframe.
- (Original) A method according to claim 1, wherein generating a track indicator comprises 8. jointly encoding track information for tracks in both subframes.
- (Currently Amended) An article of manufacture comprising a machine-accessible medium having content to provide instructions to cause a device to:

encode a first and a second subframe of a frame of data, each subframe having multiple tracks;

identify one of the multiple tracks for each subframe; and generate a track indicator to indicate to a decoder the identified track for both subframe.

- 10. (Original) An article of manufacture according to claim 9, wherein a track has pulse positions, wherein the content to provide instructions to cause the device to encode subframes having multiple tracks comprises the content to provide instructions to cause the device to encode subframes having at least one track with an additional pulse position as compared to another track, and wherein the content to provide instructions to cause the device to identify one of the multiple tracks for each subframe comprises the content to provide instructions to cause the device to identify the at least one track with the additional pulse position.
- (Original) An article of manufacture according to claim 9, wherein the content to provide 11. instructions to cause the device to encode the subframes having multiple tracks comprises the

Application No.: 10/741,304 Examiner: J.B.Jeanglaude Attorney Docket No.: 42P17107 Art Unit: 2819

content to provide instructions to cause the device to encode subframes having multiple tracks in a sequence of track locations, and wherein the content to provide instructions to cause the device to identify one of the multiple tracks for each subframe comprises the content to provide instructions to cause the device to identify the track location of one of the multiple tracks for each subframe, and wherein the content to provide instructions to cause the device to generate the track indicator comprises the content to provide instructions to cause the device to generate a set of bits that corresponds to the track locations for all of the identified tracks for both subframes.

- 12. (Original) An article of manufacture according to claim 11, wherein the content to provide instructions to cause the device to generate the set of bits comprises the content to provide instructions to cause the device to generate a set of bits that corresponds to an ordered pair, a value of the first member of the pair to indicate the identified track in the first subframe, and the value of the second member of the pair to indicate the identified track in the second subframe.
- . 13. (Currently Amended) An encoding apparatus comprising:

a receiver to receive a data stream;

processing logic to encode the data stream into a frame of data, the frame of data to have a first and a second subframe, each subframe to have multiple tracks, and the processing logic to identify one of the multiple tracks for each subframe of the received frame of data, and generate a track indicator having information to indicate to a decoder the identified track for both subframes; and

a transmitter responsive to the processing logic to transmit the generated track indicator.

Application No.: 10/741,304

Examiner: J.B.Jeanglaude Attorney Docket No.: 42P17107 Art Unit: 2819 -4-

- 14. (Original) An encoding apparatus according to claim 13, wherein the processing logic encodes a frame of data having multiple tracks with pulse positions, and encodes at least one track to have an additional pulse position as compared to another track, and wherein the processing logic identifies the at least one track with the additional pulse position.
- 15. (Original) An encoding apparatus according to claim 13, wherein the processing logic encodes a frame having subframes having multiple tracks in a sequence of track locations and identifies the track location of one of the multiple tracks for each subframe, and wherein the processing logic generates a set of bits that corresponds the track locations for all of the identified tracks for both subframes.
- 16. (Original) An encoding apparatus according to claim 15, wherein the processing logic generates a set of bits that corresponds to an ordered pair, a value of the first member of the pair to indicate the identified track in the first subframe, and the value of the second member of the pair to indicate the identified track in the second subframe.
- (Currently Amended) A coding system comprising: 17.

a speech encoder having:

a receiver to receive a data stream:

processing logic to encode the data stream into a frame of data, the frame of data to have a first and a second subframe, each subframe to have multiple tracks, and the processing logic to identify one of the multiple tracks for each subframe of the received frame of data, and generate a track indicator having information to indicate to a decoder the identified track for both subframes; and a transmitter responsive to the processing logic to transmit the generated track indicator; and

Application No.: 10/741,304 Examiner: J.B.Jeanglaude Art Unit: 2819 Attorney Docket No.: 42P17107

a transmission line coupled with the transmitter to transport the generated track indicator.

18. (Original) A coding system according to claim 17, wherein the processing logic encodes

a frame of data having multiple tracks with pulse positions, and encodes at least one track to

have an additional pulse position as compared to another track, and wherein the processing logic

identifies the at least one track with the additional pulse position.

19. (Original) A coding system according to claim 17, wherein the processing logic encodes

a frame having subframes having multiple tracks in a sequence of track locations and identifies

the track location of one of the multiple tracks for each subframe, and wherein the processing

logic generates a set of bits that corresponds the track locations for all of the identified tracks for

both subframes.

20. (Original) A coding system according to claim 19, wherein the processing logic generates

a set of bits that corresponds to an ordered pair, a value of the first member of the pair to indicate

the identified track in the first subframe, and the value of the second member of the pair to

indicate the identified track in the second subframe.

Examiner: J.B.Jeanglaude Application No.: 10/741,304 Art Unit: 2819 Attorney Docket No.: 42P17107 -6-